

FIG. 1

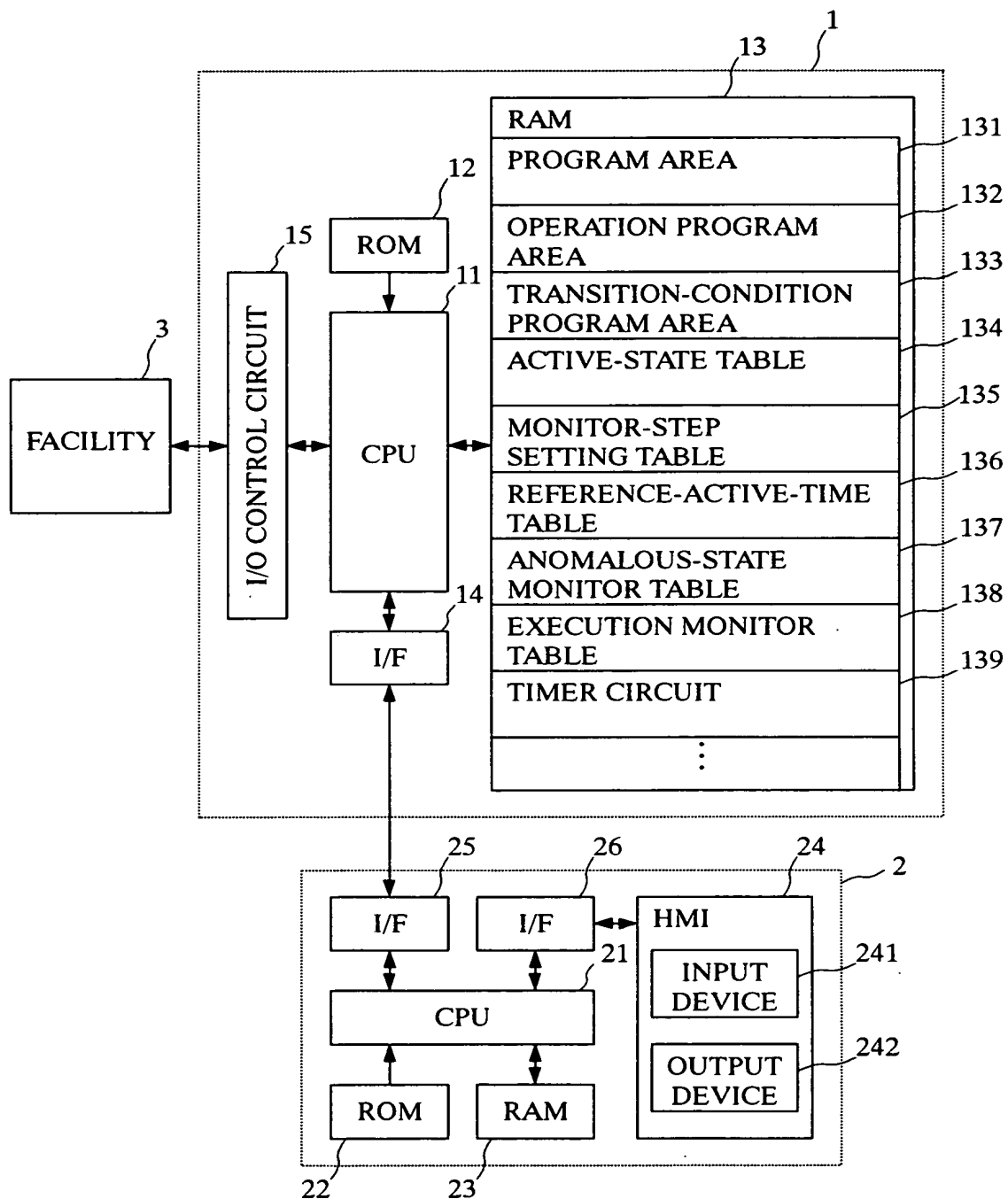


FIG. 2

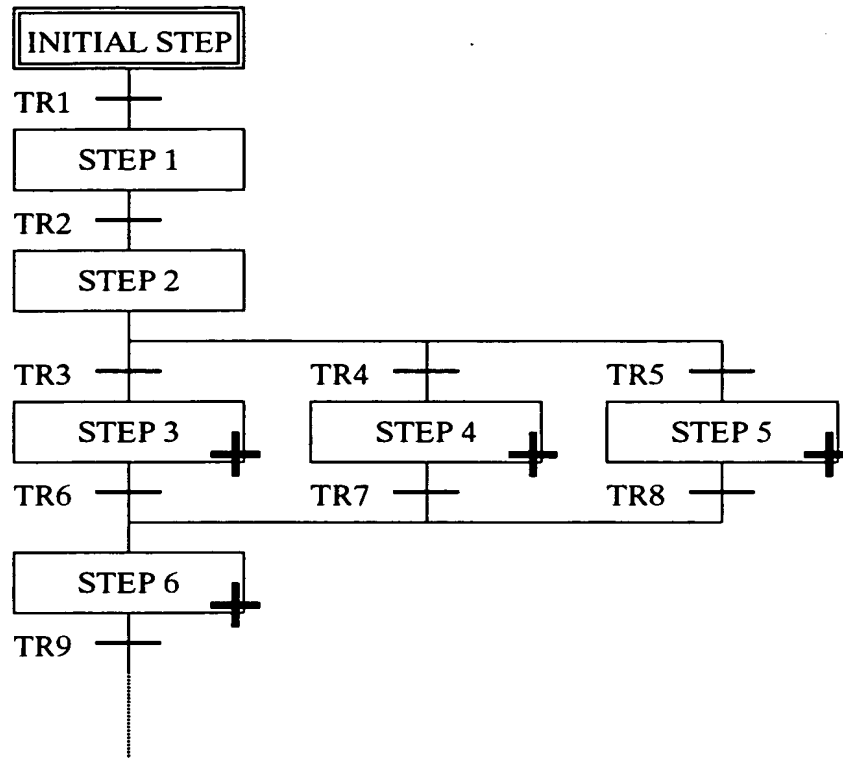


FIG. 3

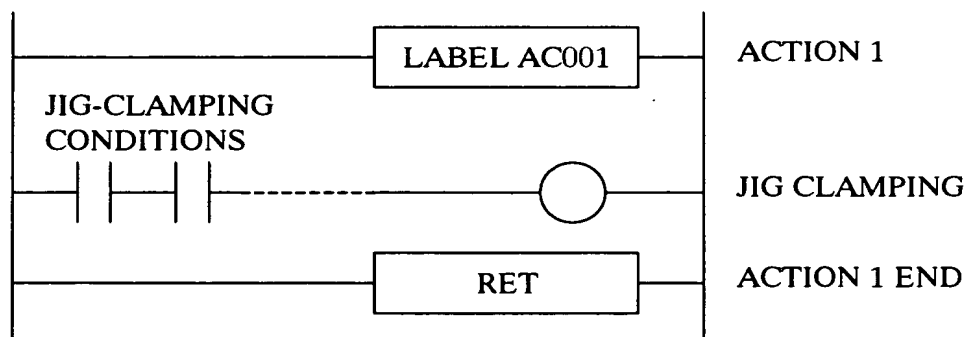


FIG. 4

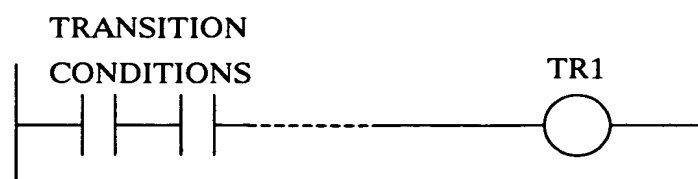


FIG. 5

STEP	ACTIVE FLAG
INITIAL STEP	0
STEP 1	0
STEP 2	0
STEP 3	0
STEP 4	0
STEP 5	0
STEP 6	1
⋮	

FIG. 6

STEP	MONITOR FLAG
INITIAL STEP	0
STEP 1	0
STEP 2	0
STEP 3	1
STEP 4	1
STEP 5	1
STEP 6	1
⋮	

FIG. 7

STEP	REFERENCE ACTIVE TIME
INITIAL STEP	
STEP 1	
STEP 2	
STEP 3	10.0
STEP 4	10.0
STEP 5	10.0
STEP 6	5.0
⋮	

FIG. 8

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STEP	ANOMALOUS- STATE FLAG
INITIAL STEP	0
STEP 1	0
STEP 2	0
STEP 3	0
STEP 4	0
STEP 5	0
STEP 6	1
• • •	

FIG. 9

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STEP	EXECUTION- COMPLETION FLAG
INITIAL STEP	1
STEP 1	1
STEP 2	1
STEP 3	0
STEP 4	1
STEP 5	0
STEP 6	0
• • •	

FIG. 10

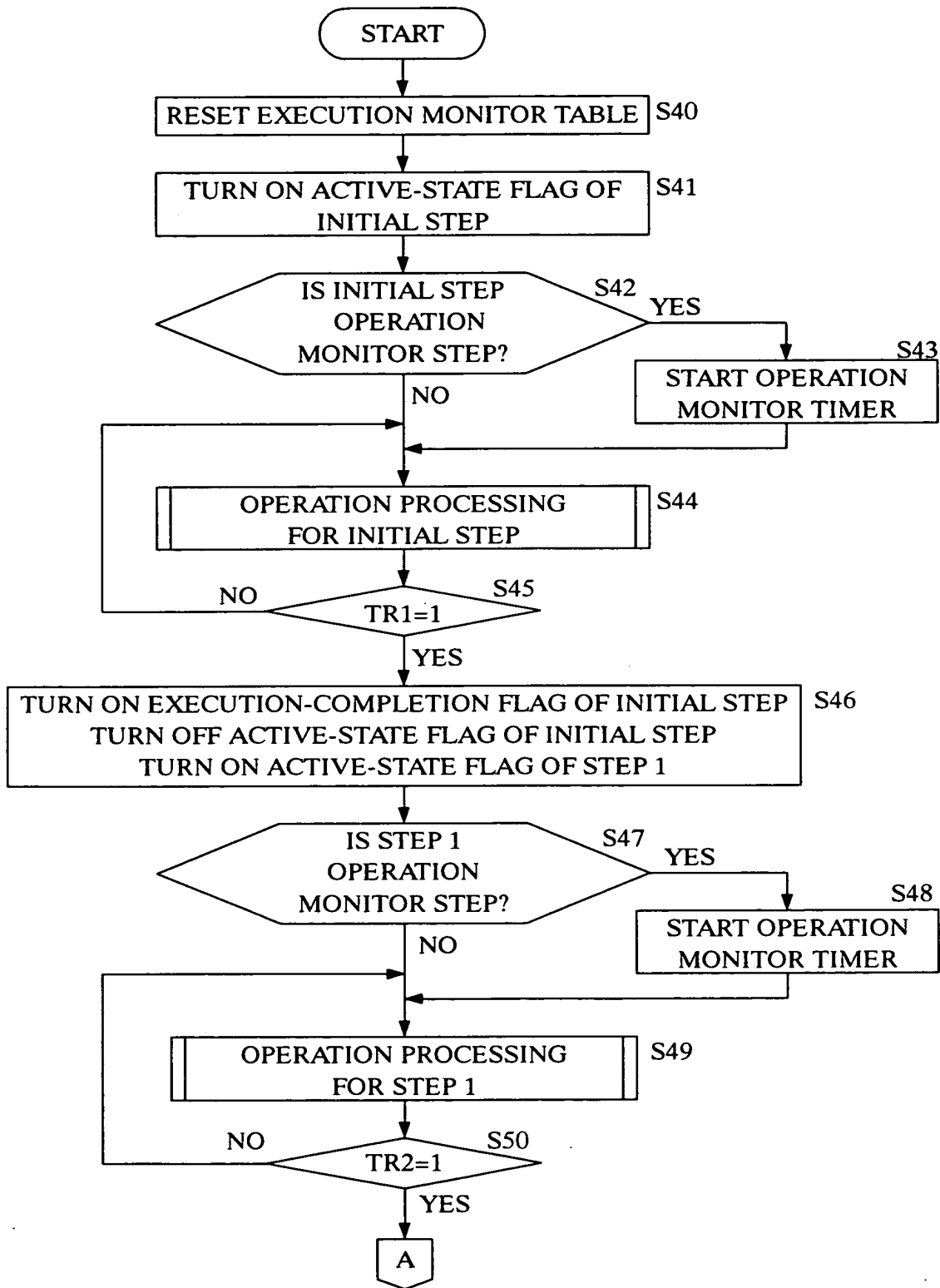


FIG. 11

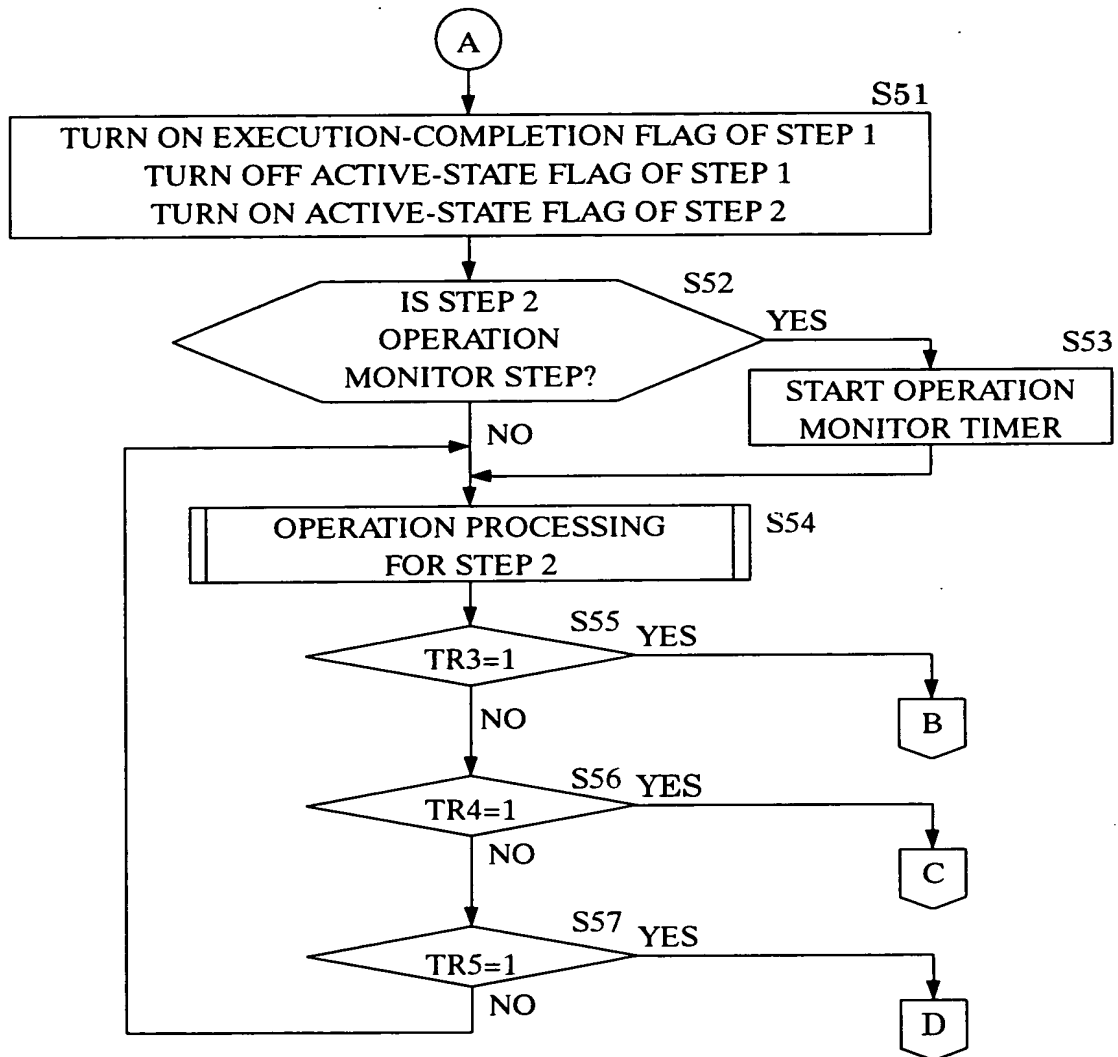


FIG. 12

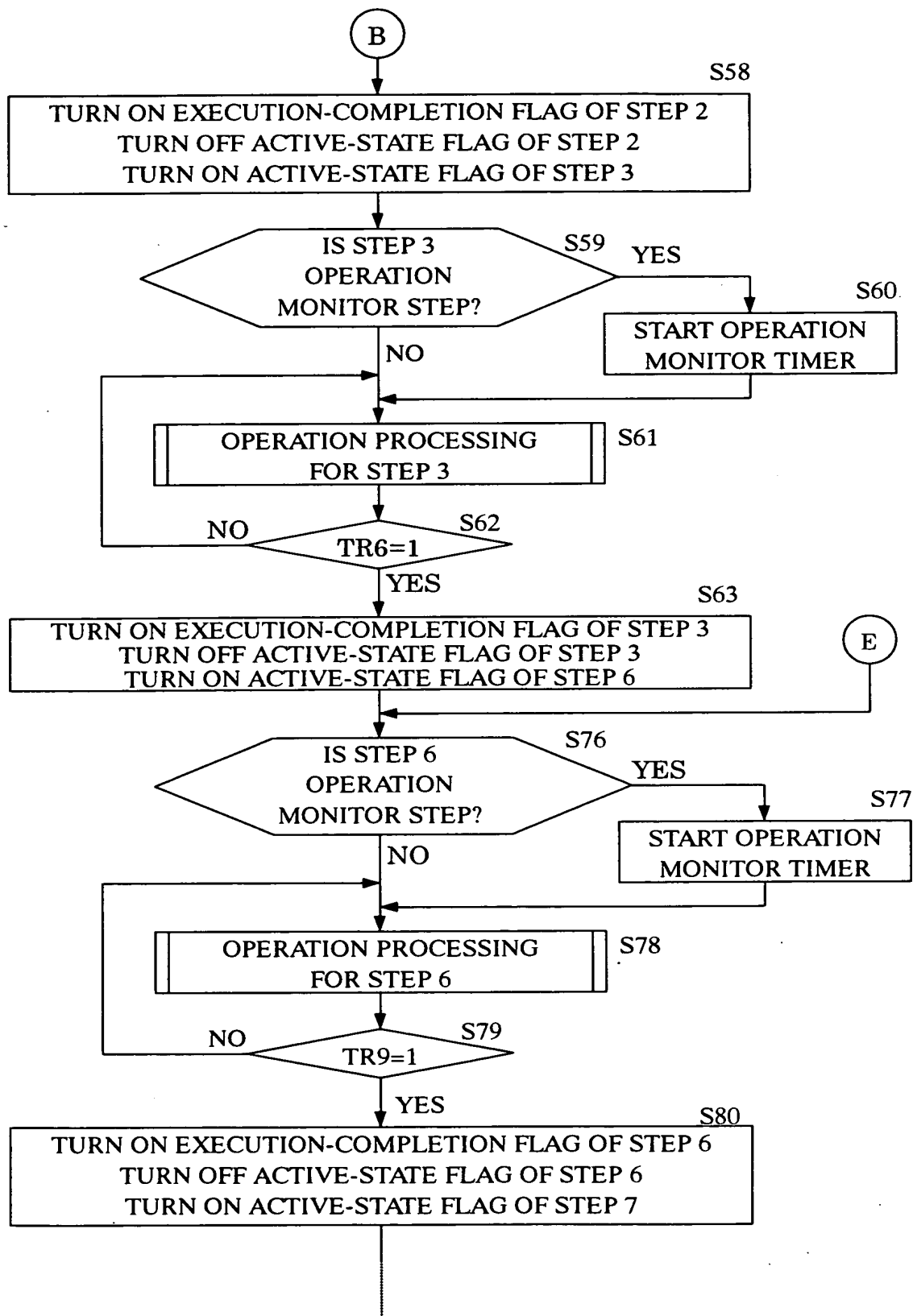


FIG. 13

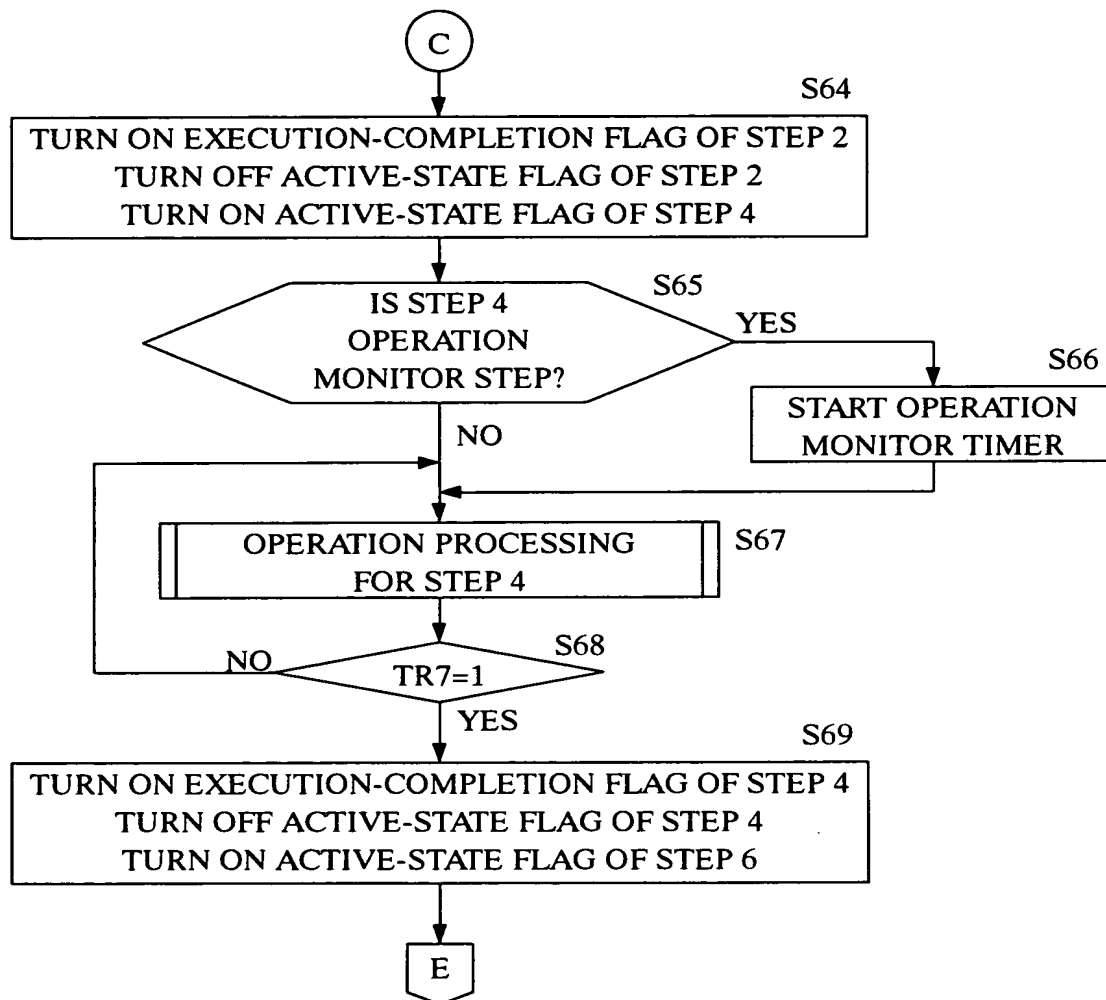




FIG. 14

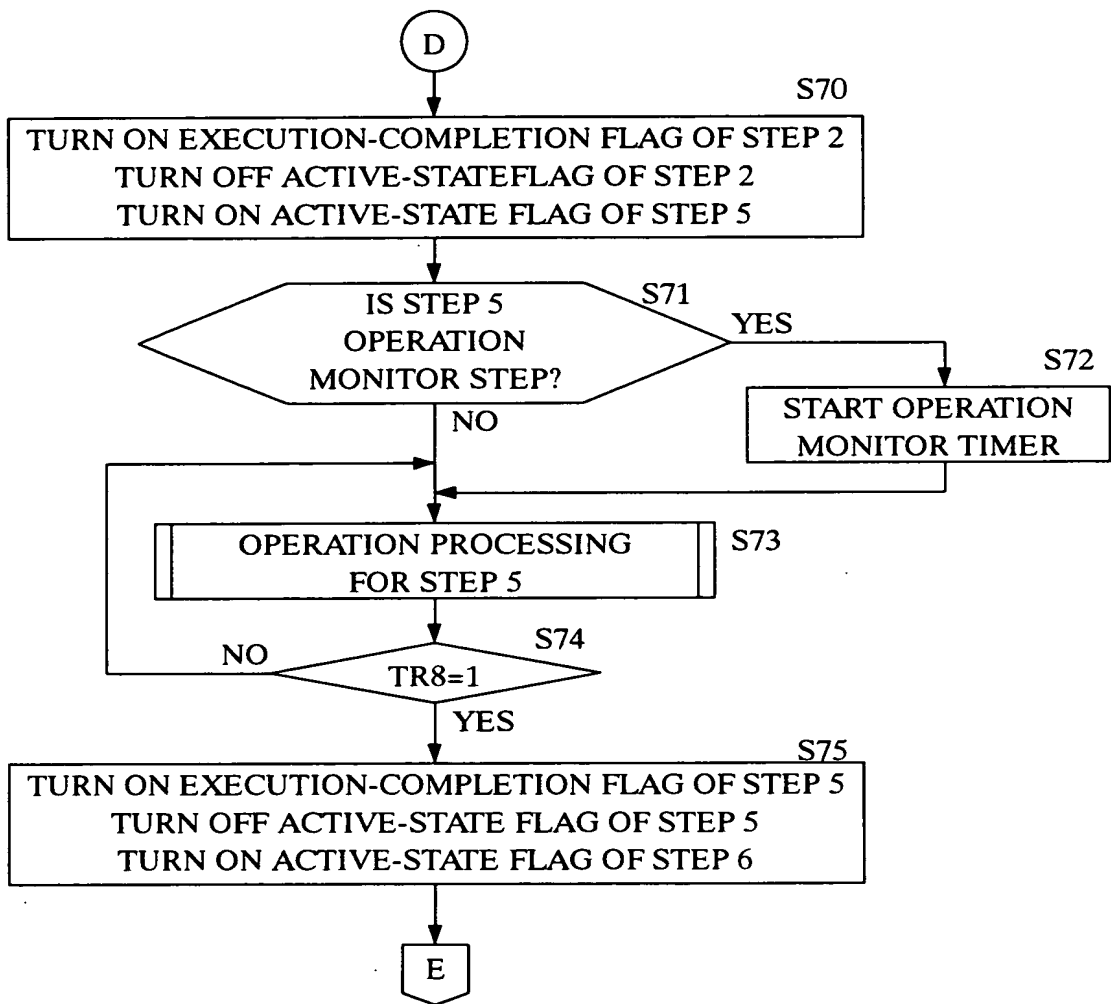


FIG. 15

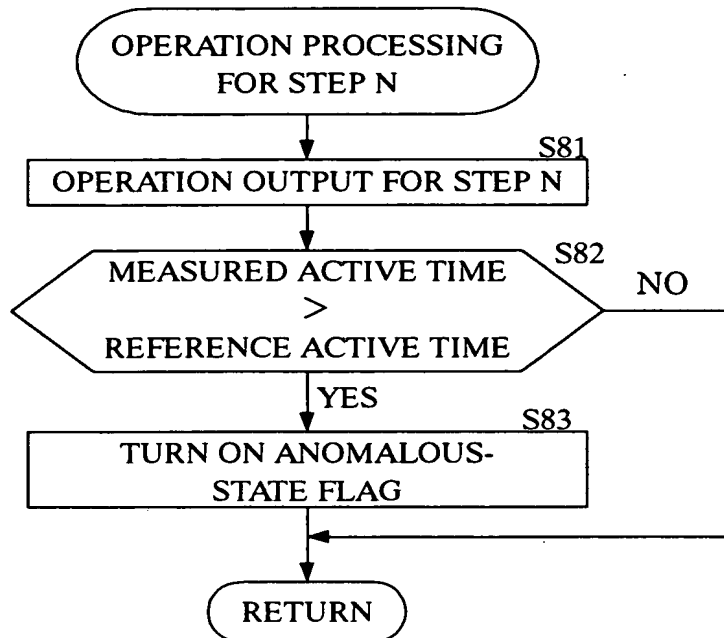


FIG. 16

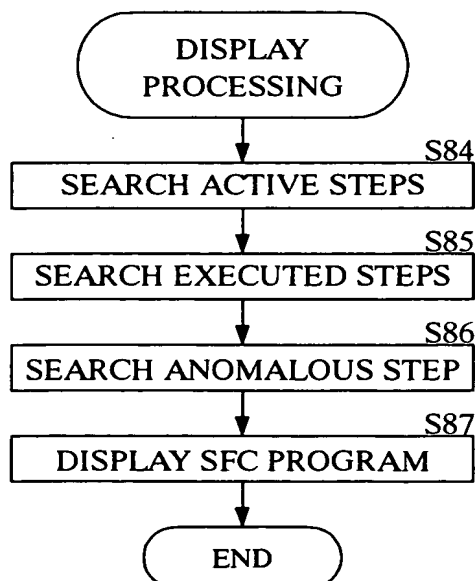


FIG. 17

STEP	ACTIVE FLAG	EXECUTION- COMPLETION FLAG	ANOMALOUS- STATE FLAG
INITIAL STEP	0	1	0
STEP 1	0	1	0
STEP 2	0	1	0
STEP 3	0	0	0
STEP 4	0	1	0
STEP 5	0	0	0
STEP 6	1	0	1
• • •			

FIG. 18

